4V Drive Nch MOS FET

RSR025N03

Structure

Silicon N-channel MOS FET

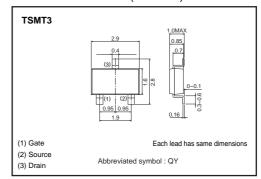
● Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).

Application

Power switching, DC / DC converter.

●External dimensions (Unit : mm)



Packaging specifications

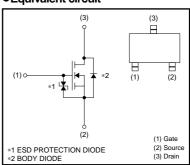
	Package	Taping	
Type	Code	TL	
	Basic ordering unit (pieces)	3000	
RSR025N03	0		

● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		Voss	30	V	
Gate-source voltage		V _{GSS}	20	V	
Drain current	Continuous	ID	±2.5	Α	
	Pulsed	I _{DP} *1	±10	Α	
Source current	Continuous	Is	0.8	Α	
(Body diode)	Pulsed	Isp*1	3.2	Α	
Total power dissipation		P _D *2	1	W	
Channel temperature		Tch	150	°C	
Storage temperature		Tstg	-55 to 150	°C	

^{*1} Pw≤100µs, Duty cycle≤2% *2 Mounted on a ceramic board.

●Equivalent circuit



A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	125	°C/W

^{*2} Mounted on a ceramic board.



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	10	μΑ	Vgs=20V, Vds=0V
Drain-source breakdown voltage	V _{(BR) DSS}	30	_	_	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	_	1	μΑ	V _{DS} =30V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1.0	_	2.5	V	V _{DS} =10V, I _D =1mA
		_	50	70		I _D =2.5A, V _{GS} =10V
Static drain-source on-state resistance	R _{DS (on)} *	_	74	105	mΩ	I _D =2.5A, V _{GS} =4.5V
resistance		_	83	118		I _D =2.5A, V _{GS} =4V
Forward transfer admittance	Y _{fs} *	1.5	_	_	S	I _D =2.5A, V _{DS} =10V
Input capacitance	Ciss	_	165	_	pF	V _D s=10V
Output capacitance	Coss	_	55	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	_	35	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	-	6	_	ns	I _D =1.25A, V _{DD} ≒15V
Rise time	t r *	-	10	_	ns	V _{GS} =10V
Turn-off delay time	t _{d (off)} *	_	20	_	ns	R _L =12.0Ω
Fall time	t _f *	-	5	_	ns	R _G =10Ω
Total gate charge	Qg *	_	2.9	4.1	nC	V _{DD} ≒15V
Gate-source charge	Qgs *	_	0.8	_	nC	V _{GS} =5V
Gate-drain charge	Q _{gd} *	_	0.9	_	nC	ID=2.5A

^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	_	_	1.2	V	Is=3.2A, Vgs=0V

^{*}Pulsed

•Electrical characteristic curves

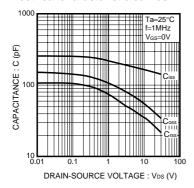


Fig.1 Typical Capacitance vs. Drain-Source Voltage

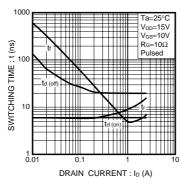


Fig.2 Switching Characteristics

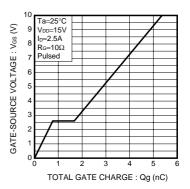


Fig.3 Dynamic Input Characteristics

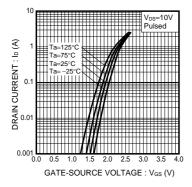


Fig.4 Typical Transfer Characteristics

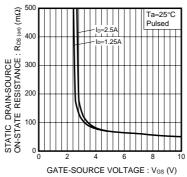


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

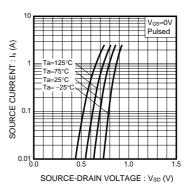


Fig.6 Source Current vs. Source-Drain Voltage

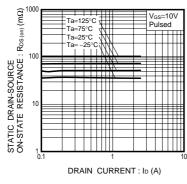


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

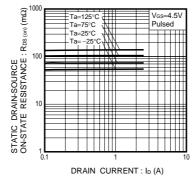


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

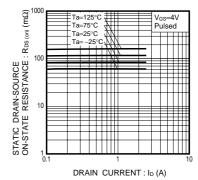


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

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